

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2876  
Examiner:Lee, M.

In re Patent Application of

Applicants : Isao KUDO

Appln. No. : 09/053,040

Filed : April 1, 1998

For : SEMICONDUCTOR DEVICE AND AN  
INFORMATION MANAGEMENT  
SYSTEM THEREFOR

Atty. Dkt. : 31759-138000



**AMENDMENT**

June 21, 2000

Assistant Commissioner for Patents  
Washington, D.C. 22031

Sir:

In response to the Office Action of March 21, 2000 please amend this application as follows:

**IN THE CLAIMS:**

Please amend claims 1-20 as follows:

1. (Twice Amended) A semiconductor device having a plurality of semiconductor chips arrayed on a surface of a wafer, at least one of said semiconductor chips having a two-dimensional barcode pattern for information management provided on a surface of said at least one [each of] semiconductor chip [chips arrayed on a surface of said wafer surface as] with the pattern representing chip ID information, and said two-dimensional barcode pattern is comprised of a plurality of blocks arranged in a predetermined two-dimensional region.